

A100 Disk Drive

The following table lists signals routed between the A100 Disk Drive assembly and the A7 CPU assembly. This table shows several things — if the assembly generates or uses the signal or voltage, and if a signal is bidirectional. A description of each signal follows the table.

Signal Name	Pin(s)	A7 P3	A100
DIR	17	S	•
DISKIN _n	22	•	S
DRIVESEL _n	21, 29	S	•
DSKCHG _n	1	•	S
HDSEL	3	S	•
HIDENS _n	33	•	S
INDEX _n	27	•	S
MTR _n	19, 31	S	•
ReDATA _n	5	•	S
SELO	25	S	•
SEL1	23	S	•
STEP _n	15	S	•
T00 _n	9	•	S
WDATA	13	S	•
WGATE	11	S	•
WRIPROT _n	7	•	S
+5 V	24, 26, 28	•	•
Gnd	2-20 (even)	•	•
Not Used	30, 32, 34	—	—

- S This assembly is the source of the signal.
- This assembly uses the signal.
- This assembly does not use this signal.